Saleh Mohamed Eisa Ahmed

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Personal Data	Name : Saleh Mohamed Eisa Ahmed	
	Languages: Arabic and English	
Contact Information	Address: Department of Electronics and Communications Engineering Collage of Engineering and Technology Arab Academy for Science and Technology and Maritime Transport Smart Village Campus P.O Box: 12577 B2401 6 th of October, Egypt	
	Phone: +2-02-3570882/3 E-mail: <u>saleheisa@aast.edu</u>	
<u>Employment</u>	Vice Dean for students' affairs (2020-present)Arab Academy for Science and Technology and Maritime Transport, Department of Electronics & Communications Engineering (Smart Village Campus)Acting Head (2017-2020) Arab Academy for Science and Technology and Maritime Transport, Department of Electronics & Communications Engineering (Smart Village Campus)Assistant Professor (2014-2017) 	

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Education	PhD in Electronics & Communications - December 2014 Ain Shams University Cairo Egypt December 2014 Dissertation: "Design, Implementation and Charactraization of an UWB Transmitter"
	 M.Sc. Electronics & Communications - December 2006 <i>Arab Academy for Science and Technology and Maritime Transport,</i> <i>Electronics & Communications Engineering Department (Cairo)</i> <u>Thesis:</u> "Design, Implementation and Characterization of Direct Digital Frequency <i>Synthesizer</i> P.Se. Electronics & Communications March 2000
	B.Sc. Electronics & Communications – March 2000 Arab Academy for Science and Technology and Maritime Transport, Electronics & Communications Engineering Department (Alexandria) Excellent with Honour <u>Graduation Project:</u> "Fibber-Optic Cables in Local Subscriber Network
<u>Scholarships and</u> <u>Awards</u>	• One of the winning papers in the technical paper contest. Awarded in: Mentor Graphic Egypt " <i>Higher Education Program 3rd annual Workshop</i> "- December 2007.
	• Award in Course: "VLSI Based communication application Design", sponsored by the Egyptian Ministry of Telecommunications and supervised by the National Telecommunications Institute (NTI).
	Honour: during the Undergraduate Studies (Arab Academy for science and Technology)

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<u>Teaching</u>	Arab Academy for Science and Technology, Electronics & Communications Engineering Dept.	
	 EE231-Electrical Circuits 1 EC238 -Electronics 1 EC339 - Electronic 2 EC332 -Electronic Devices 2 EC333 - Electronic Amplifiers EC334 - Analog and Digital Circuits Analysis EC341 -Electromagnet I EC432- Microelectronic Circuits EC434- Analog Signal Processing 	
	 EC535 - Digital VLSI Design EC538- Selected Topics in Electronics EC560- Electronic Circuits for Communications EC738 Advanced Electronic Devices EC760 Advanced Engineering Mathematics EC764 Electronic Seminars 	
<u>Research Interests:</u>	 Analog and Digital VLSI design Low power design VHDL-based system design. Printed Electronics 	

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<u>Publications</u>	 Rehab Ali, Hossameldin Eassa, Hesham H. Aly, Mohamed Abaza, and Saleh M. Eisa, "Low Power FPGA Implementation of a Smart Building Free Space Optical Communication System", Photonics 2022, Vol. 9, no. 6, 432, 2022. https://doi.org/10.3390/photonics9060432
	 Hesham Tarek, Hesham H Aly, Saleh Eisa, Mohamed Abul-Soud,, "Optimized Deep Learning Algorithms for Tomato Leaf Disease Detection with Hardware Deployment", Electronics, Vol.11, no.1, 140, 2022. https://doi.org/10.3390/electronics11010140
	• H. Mostafa, S. M. Eisa, H. H. Issa, N. H. Shaker, "Lightweight Hybrid Encryption System with FPGA Design Proposal", IOP Conference Series: Materials Science and Engineering, Vol.1, no. 1051, pp. from 012- to 023. (2021)
	• Joumana Dakkak, Saleh Eisa, Hesham M. El-Badawy and Ahmed ElBakly "Analysis for Joint Delay-Power Tradeoff with Buffer/Channel-Aware and its FPGA Implementation in Wireless Sensor Networks," Sensors Journal, vol. 20, no. 3114, 2020. doi:10.3390/s20113114, ISSN 1424-8220.
	 N.K. Shebl, S.M. Eisa, H.H Issa, K.A Shehata, "Low Power BCH Decoder Using Verification Algorithm and Two-Step Parallel Chien Search Architecture", In: Arai K., Kapoor S., Bhatia R. (eds) Advances in Information and Communication. FICC 2020. Advances in Intelligent Systems and Computing, vol 1129. Springer, Cham.
	 H. H. Issa and S. M. Eisa Ahmed, "FPGA Implementation of Floating Point Based Cuckoo Search Algorithm," in IEEE Access, vol. 7, pp. 134434-134447, 2019. doi: 10.1109/ACCESS.2019.2942205 Mohamed Hakim, Saleh Eisa, Khaled Shehata, Hany fikry, "Design of X-Band Low Noise Amplifier For Radar Applications", Journal of Physics: Conference Series 1447 (2020) 012036 IOP Publishing doi:10.1088/1742-6596/1447/1/012036, 2020.
	 M. S. Badran, H. H. Issa, S. M. Eisa and H. F. Ragai, "Low Leakage Current Symmetrical Dual-k 7 nm Trigate Bulk Underlap FinFET for Ultra Low Power Applications," in IEEE Access, vol. 7, pp. 17256-17262, 2019, doi: 10.1109/ACCESS.2019.2895057.

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- .Tadros, S. Eisa, H. H. Issa and K. Shehata, "Modified Scaled Min Sum LDPC Decoder for DVB-S2/S2X/T2," 2018 30th International Conference on Microelectronics (ICM), Sousse, Tunisia, 2018, pp. 172-175. doi: 10.1109/ICM.2018.8704096 ISBN: 978-1-5386-8167-1
- Noha K. Shebl, Saleh M. Eisa, Hanady H. Issa, and Khaled A. Shehata, "A Low Power BCH Based on Error Locator Polynomial Searching Algorithm and Two Step Chien Search Algorithm," in Proc. 3rd International Conference on Advanced Technology and Applied Sciences (ICaTAS), Sepang, Malaysia, Oct.. 2018.
- Jumana Abu-Khalaf, Razan Saraireh, Saleh Eisa, and Alaaldeen Al-Halhouli, "Experimental Characterization of Inkjet-Printed Stretchable Circuits for Wearable Sensor Applications," Sensors Journal, vol. 18, no. 10,3476, 2018. doi:10.3390/s20113114, ISSN 1424-8220.
- Mahmoud S. Badran, Hanady H. Issa, Saleh M. Eisa and Hani F. Ragai, "Low power 7 nm FinFET based 6T-SRAM design," 2nd International Conference on Advanced Technology and Applied Sciences (ICaTAS), Alexandria, Egypt, Sep. 2017
- S. M. Eisa, H. H. Issa, K. a. Shehata, and H. F. Ragai, "Design and Analysis of a Low Power UWB Pulse Generators," Int. J. Comput. Electr. Eng., vol. 6, no. 3, pp. 244– 247, 2014, doi: 10.17706/IJCEE
- H. H. Issa, S. M. Eisa, K. A. Shehata and H. F. Ragai, "Srd-based pulse generator for UWB wireless network applications," 2013 International Conference on Computer Applications Technology (ICCAT), Sousse, 2013, pp. 1-4. doi: 10.1109/ICCAT.2013.6522049
- Khaled Shehata, Saleh Eisa and Hani Fikry," Design and Implementation of an 11bit Nonlinear Interpolation DAC," International Journal of Electronics, vol. 95, no. 3, p. 177-192, Jan. 2008.
- Khaled Shehata, Saleh Eisa and Hani Fikry," Design and Implementation of an 11bit Nonlinear Interpolation DAC," Mentor Graphics Egypt Higher Educational Program 3rd annual workshop, December 2007.

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 S. M. Eisa, K. A. Shehata and H. F. Ragai, "Design and implementation of an 11-bit non-linear interpolation DAC," International Conference on Design and Test of Integrated Systems in Nanoscale Technology, 2006. DTIS 2006., Tunis, 2006, pp. 136-139. doi: 10.1109/DTIS.2006.170866