

# **CURRICULUM VITE of**

## **Name: Dr. Albashir Adel Youssef**

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### **OBJECTIVE**

- Post-Doc. in prestigious university, continue research and teaching in it.

### **EDUCATION**

**PhD. in Electronics and Communications Engineering**, May 2019, Ain Shams University, Cairo, Egypt.

**M. Sc. in Electronics and Communications Engineering**, July, 2011, Arab Academy for Science, Technology and Maritime Transport, Cairo, Egypt.

**B. Sc. in Electronics and Communications Engineering**, Sept, 2008, Arab Academy for Science, Technology and Maritime Transport, Cairo, Egypt.

### **RESEARCH AREAS**

Digital Signal Processing, Digital Communications, Wireless communication PHY and MAC layer, Information Theory, Error Control Coding, WBANs, Virtual MIMO and Blind Channel Estimation.

### **EMPLOYMENT**

• **July 2019 – Current:** Lecturer, Electronics and Communication Engineering Dept, Faculty of Engineering and Technology, Arab Academy for Science, Technology and Maritime Transport, Cairo, Egypt.

• **July 2011 – July 2019:** Teaching Assistant, Electronics and communications Engineering Dept., Faculty of Engineering and Technology, Arab Academy for Science, Technology and Maritime Transport, Cairo, Egypt.

• **Sept 2008 – July 2011:** Graduate Teaching Assistant, Electronics and communications Engineering Dept., Faculty of Engineering and Technology, Arab Academy for Science, Technology and Maritime Transport, Cairo, Egypt.

## **TEACHING**

- EC322: Introduction to Communication Systems.
- EC321: Signals and Systems.
- EC334: Analog and Digital Circuit Analysis.
- EC421: Statistical Communication Theory.
- EC432: Analog Signal Processing.
- EC210: Electronic Devices 1.
- EC238: Electronics 1.
- EC333: Microelectronic Circuits.
- EC422: Introduction to Digital Communications.
- EC 217: Measurements and Instrumentation.
- EC 410: Electronic Measurements.
- EC536: VLSI Fabrication & Testing.
- EC535: Digital VLSI Design.
- EC533: Digital Signal Processing.

## **SCIENTIFIC ACTIVITIES**

- Member in the Institute of Electrical and Electronics Engineers (IEEE).
- Reviewer of EURASIP Journal on Wireless Communications and Networking.
- Reviewer of IEEE Access Journal.

## **PUBLICATIONS**

### **1. Journals:**

Albashir Adel Youssef, Bassant Abdelhamed, Salwa Hussein El-Ramly, Hussein El-Attar and Hazem Hassan Ali,” Joint VMIMO and LDPC Decoders for IR-UWB Wireless Body Area Network”, IEEE Access, vol. 7, pp. 4400-4409, 2019.

Albashir Adel Youssef, Bassant Abdelhamed, Salwa Hussein El-Ramly, Hussein El-Attar and Hazem Hassan Ali,” LDPC Decoding Algorithms for Implant to Implant Wireless Body Network”, IEEE Access, vol. 6, pp. 13200 – 13212, 2018.

Albashir A. Mohamed, Maha M. Elsabrouty, and Salwa H. El-Ramly, "Bootstrapped Low Complexity Iterative Decoding Algorithm for Low Density Parity Check (LDPC) Codes", Canadian Journal on electrical and electronics engineering, vol. 1, no. 1, pp. 10-14, Feb. 2010.

## **2. Conferences:**

Albashir A. Mohamed, Maha M. Elsabrouty, and Salwa H. El-Ramly, "Bootstrapped Iterative Decoding Algorithms for Low Density Parity Check (LDPC) Codes", Fifth International Conference on Systems and Networks Communications (ICSNC), Nice, France, pp. 335 – 339, 2010.

Albashir A. Mohamed, Maha M. Elsabrouty, and Salwa H. El-Ramly, "Bootstrapped Low Complexity Iterative Decoding Algorithm for Low Density Parity Check (LDPC) Codes", Mobilware 2010, LNICST 48, Chicago, USA, pp. 377--389. Institute for Computer Sciences, Social Informatics and Telecommunications Engineering (2010).

## **COMPETITIONS**

- Robocon Egypt 2006.

## **AWARDS**

- Full scholarship to obtain M.Sc. in Electronics and Communications Engineering also work as Graduate Teaching Assistant.

## **PROJECTS**

### **Past Projects:**

- "Implementation of WiMAX Physical Layer on FPGA", finished July 2008, graduation project.
- "Implementation of LTE Physical Layer ", finished July 2009, co-supervised with Dr. Maha El-sabrouty.
- "Implementation of LDPC Decoder on FPGA", finished March 2010, co-supervised with prof. khaled Shehata and Dr. Hanady Issa.
- "Implementation of Viterbi Decoder on FPGA", finished Sept. 2011, co supervised with prof. khaled Shehata and Dr. Hanady Issa.

## **Research Accomplishments**

- ✓ Strong background for LDPC decoding algorithms.
- ✓ Regular background for Turbo Codes and MAP decoding.
- ✓ Strong background for convolutional codes and Viterbi decoding.
- ✓ VHDL programming language.
- ✓ Excellent experience in VLSI design using VHDL.
- ✓ Experience in teaching VHDL programming (participated in teaching team of course offered for graduate engineers from AASTMT and Ministry of Communications and Information Technology in Egypt).
- ✓ Excellent experience in Matlab scripting.
- ✓ Regular experience in C programming.
- ✓ Strong Latex scripting